

Candidate Information

Position: Research Fellow in Semiconductor Security
School/Department: School of Electronics, Electrical Engineering and Computer Science
Reference: 26/113146
Closing Date: Monday 16 February 2026
Salary: £41,519 - £49,536 per annum.
Anticipated Interview Date: Monday 2 March 2026
Duration: Until 31 August 2028

JOB PURPOSE:

We are seeking a highly motivated postdoctoral research fellow to conduct research into hardware and semiconductor security. The position will be hosted within the Centre for Secure Information Technology (CSIT: <https://www.qub.ac.uk/research-centres/csit/>) at Queen's University Belfast.

As this is a re-advertisement, previous applicants need not apply as their applications will not be considered.

MAJOR DUTIES:

1. Conduct research into hardware and semiconductor security focusing on areas such as side-channel analysis, fault attacks, hardware Trojan detection, microarchitectural attacks, AI hardware security and/or security verification within Electronic Design Automation (EDA) tools.
2. Actively contribute to the general planning and delivery of the overall research project activities.
3. Present progress reports on research for funders, to CSIT industry advisory board members or to external audiences to disseminate and publicise research findings.
4. Prepare, in consultation with supervisor, material for publication in national and international journals and presentations at international conferences.
5. Assist in the preparation of funding proposals and applications to external bodies.
6. Carry out routine administrative tasks associated with the research project. This might include organisation of project meetings and documentation, financial control, risk assessment of research activities.
7. Carry out occasional undergraduate/postgraduate student project supervision, demonstrating or lecturing duties within the post holder's area of expertise and under the direct guidance of a member of academic staff.
8. Read academic papers, journals and textbooks to keep abreast of developments in own specialism and related disciplines.
9. Any other duties that may reasonably be requested by the programme supervisor.

ESSENTIAL CRITERIA:

1. 2:1 Honours degree in Electrical and Electronic Engineering/Computer Science/Mathematics (or related discipline).
2. Have, or be about to obtain, a PhD in a relevant subject.
3. Specific, relevant research experience to include:
 - Hardware security/semiconductor security;
 - Evidence of a strong publication record commensurate with career stage and experience;
 - Expertise in EDA tools.
4. Demonstrable ability to contribute to broader management and administrative processes.
5. Demonstrable ability to contribute to the School's outreach programme by establishing links with industry, community groups etc.
6. Sufficient breadth and depth of specialist knowledge in the discipline and of research methods and techniques to work within established research programmes.
7. Good written and verbal communication skills.
8. Proven ability to communicate complex information clearly.

9. Demonstrable ability to innovate and rapidly contribute to research projects.
10. Willingness to visit collaborative partners and to attend meetings and conferences nationally and internationally as requested.

DESIRABLE CRITERIA:

1. Expertise in one or more of the following areas: side-channel analysis, fault attacks, hardware Trojan detection, microarchitectural attacks, AI hardware security. Expertise in security verification within Electronic Design Automation (EDA) tools. Expertise in VLSI design.
2. Ability to build contacts and participate in internal and external networks.
3. Experience of collaborative research or working in a team is desirable.