



Candidate Information

Position:	Research Fellow
School/Department:	School of Electronics, Electrical Engineering and Computer Science
Reference:	24/111699
Closing Date:	Monday 25 March 2024
Salary:	£37,841 per annum
Anticipated Interview Date:	Monday 15 April 2024
Duration:	18 months

JOB PURPOSE:

To conduct research into the design and implementation of practical, robust and physically secure post-quantum cryptographic architectures at the Centre for Secure Information Technology (CSIT), at the Institute of Electronics Communication and Information Technologies (ECIT), Queen's University Belfast. This research is part of the INNOVATE-funded project called SCHEME (Safety Critical Harsh Environment Micro-processing Evolution), led by Rolls Royce.

MAJOR DUTIES:

1. Conduct research into the design and implementation of practical, robust and physically secure post-quantum cryptographic (PQC) architectures.
2. Actively contribute to the general planning and delivery of the overall research project activities.
3. Present regular progress reports on research to external audiences to disseminate and publicise research findings.
4. Represent CSIT at standardisation activities related to post-quantum cryptography.
5. Prepare, in consultation with supervisor, material for publication in national and international journals and presentations at international conferences.
6. Assist in the preparation of funding proposals and applications to external bodies.
7. Carry out routine administrative tasks associated with the research project. This might include organisation of project meetings and documentation, financial control, risk assessment of research activities.
8. Carry out occasional undergraduate supervision, demonstrating or lecturing duties within the post holder's area of expertise and under the direct guidance of a member of academic staff.
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10. Read academic papers, journals and textbooks to keep abreast of developments in own specialism and related disciplines.
11. Any other duties that may reasonably be requested by the programme supervisor.

ESSENTIAL CRITERIA:

1. 2:1 Honours degree in Electrical and Electronic Engineering/Computer Science/Mathematics (or related discipline)
2. Have, or be about to obtain, a PhD in a relevant subject.
3. Relevant research experience in one or more of the following:
 - embedded systems design
 - FPGA or ASIC hardware design.
4. Expertise in post-quantum cryptography.
5. Evidence of a good publication record commensurate with career stage and experience.
6. Ability to contribute to broader management and administrative processes.
7. Ability to contribute to the School's outreach programme by establishing links with industry, community groups etc.
8. Sufficient breadth and depth of specialist knowledge in the discipline and of research methods and techniques to work within established research programmes.
9. Good written and verbal communication skills.
10. Ability to communicate complex information clearly.

11. Ability to innovate and rapidly contribute to research projects.
12. Willingness to visit collaborative partners and to attend meetings and conferences nationally and internationally as requested.

DESIRABLE CRITERIA:

1. Expertise in hardware/software co-design.
2. Expertise in side-channel analysis.
3. Ability to build contacts and participate in internal and external networks.
4. Experience of collaborative research or working in a team is desirable.